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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

OFFICIAL

In re the Application of

Hoyle et al. (TI-30561)

Conf. No. 1217

Serial No. 09/702,405

Group Art Unit: 2183

Filed: October 31, 2000

Examiner: Huisman

For: Microprocessor for Having a Set of Byte Intermingling Instructions

REPLACEMENT AMENDMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Introductory Comments:

This paper is presented in response to the Notice of Non-Compliant Amendment mailed April 27, 2004. It is submitted that this paper places the Amendment filed March 8, 2004 in compliance with the Rules of Practice. Reconsideration of this application, based on that amendment, is continued to be respectfully requested.

The replacement section of the amendment to the specification begins on page 2.

The replacement section of the amendment to the claims begins on page 9.

Applicants' remarks begin on page 14.

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From: Rodney M. Anderson
Anderson, Levine, and Lintel
(fax) 972.664.9606
(voice) 972.664.9554

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MAY 03 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re the Application of

Hoyle et al.

Serial No.: 09/702,405

Filed: October 30, 2000

For: Microprocessor for Having a Set of Byte Intermingling Instructions

Docket Number: TI-30561

Art Unit: 2183

Examiner: Huisman

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by
facsimile to the U.S. Patent and Trademark Office at 703.872.9306
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Rodney M. Anderson

3 May 2004

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<input type="checkbox"/> CONTINUATION APP'N	<input type="checkbox"/> FEE TRANSMITTAL (1 page)
<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Hoyle et al.	
RECEIPT DATE & SERIAL NO.: Serial No.: 09/702.405 Filing Date: 10/30/2000	
TITLE OF INVENTION: Microprocessor for Having a Set of Byte Intermingling Instructions	
TI FILE NO.: TI-30561	DEPOSIT ACCT. NO.: 20-0668
FAXED: 3 May 2004 DUE: 27 May 2004 ATTY/SECY: ALL/RMA	

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Texas Instruments Incorporated
PO Box 655474, M/S 3999
Dallas, TX 75074